



**INDIAN INSTITUTE OF SCIENCE EDUCATION AND
RESEARCH THIRUVANANTHAPURAM [IISERTVM]**

PH.-0471 2597454,
FAX: 0471-2597427
EMAIL: purchasestores@iisertvm.ac.in

CET CAMPUS, ENGINEERING COLLEGE. P.O
THIRUVANANTHAPURAM 695016,
KERALA, INDIA

Date: 9th July 2015

ADDENDUM TO TENDER NO
No: IISER/PUR/5790/15

Dear Sirs,

Sub: HPC Cluster- Responses to Pre-bid Queries


This has reference to Pre-bid meeting held on 26th June 2015 the queries raised by the Bidders. The responses of IISERTVM are available in Annexure to this Addendum. Vendors are required to make note of these changes to our technical specifications and commercial terms of the tender notice No. IISER/PUR/5790/15 under advertisement No. IISER/PUR/PT/3/15 dated 2/6/2015.

Last date for submission of tender: 4th August 2015 [Upto 2 PM]

Date of opening of Technical bids: 4th August 2015 [at 3 PM]

Thanking You,

Yours Faithfully


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के. भास्कर राव / K. BHASKARA RAO
उप कुलसचिव [क्रय एवं भंडार]
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भारतीय विज्ञान शिक्षा एवं अनुसंधान संस्थान तिरुवनन्तपुरम
Indian Institute of Science Education and Research Thiruvananthapuram
सी.ई.टी. कैम्पस, इंजीनीयरींग कोलेज पी.ओ.
CET Campus, Engg. College P.O.

ANNEXURE TO ADDENDUM
DATED 9TH JULY 2015

ANSWERS TO THE QUERIES RECEIVED FROM VENDORS PARTICIPATED IN THE HPC TENDER

Tender No: IISER/PUR/PT/3/15

SL No	SPECIFICATION REFERENCE	QUERIES	IISERTVM'S RESPONSE
1	Due date etc		
	Tender Due Date - 13th July 2015	(Lenovo/HCL): Requesting IISER to provide extra 2 weeks extension to the tender due date submission. (Wipro): Our OEM systems are occupied with other Benchmarks We request IISER to extend the due date till 27th July.	The last date for submission of bids is upto 2PM on 4/8/2015 The date of opening of Technical bids: 4/8/2015 @ 3 PM
	The solution must be well designed so that the ratio Rmax/Rpeak is greater than 0.75, where Rmax is the sustained HPL performance defined above, obtained using exactly the same ground rules as given in http://www.top500.org/project/linpack/ .	(Lenovo/HCL): Please confirm that Rmax/Rpeak of 0.75 is expected on CPU only compute nodes (HP): Please clarify, whether optimized HPL linpack can be used. (HP): On the Hybrid nodes, we have seen close to 73% Rmax efficiency for the 20TF hybrid cluster size requested. Requesting to allow 73% Rmax on Hybrid cluster (Wipro): For GPU nodes 0.75 Rmax/Rpeak is difficult to achieve, we request to relax the Rmax/Rpeak to 0.60.	We modify this requirement as follows. Rpeak is to be calculated by leaving out the performance of the GPU cards in the hybrid nodes. Similarly 'Rmax' should be the HPL performance obtained without utilizing the GPU cards in the hybrid nodes. The ground rules for running the tests are clearly defined in the link given. We modify this requirement as follows. Rpeak is to be calculated by leaving out the performance of the GPU cards in the hybrid nodes. Similarly 'Rmax' should be the HPL performance obtained without utilizing the GPU cards in the hybrid nodes.
	IISER-TVM reserves the right to scale up or scale down the final purchase	(Wipro): As the switch asked is a chassis model, scaling up or scaling down of	The mode is clear. You will be quoting unit price of the chassis and also how


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	order in terms of total TFLOPS requirement by up to 20%. However, all the bids will be evaluated for a system having the stated requirement of 100 TFLOPS of Rpeak. In the event the final size of the order is revised as mentioned above, the revised price will be arrived at by using the unit rates quoted for the necessary items as listed in the BOQ with proportionate changes in the price of lump-sum items.	compute nodes may change the model of the chassis switch or leaf switch module in the switch. We request IISER to clarify on, how the price points will be arrived in this case.	many leafs per chassis, how many are occupied etc. So if we decide to scale up, then the need for a new chassis etc will be considered and the price will be worked out in consultation with the selected bidder as per the unit price based formula already specified.
2	PART - A Master Node		
	Processor on Master/login node	(Lenovo/HCL): Request not to specify the CPU clock speed	We have only said 2.5 GHz or better. So better speed can be provided
3	PART - B CPU-only Compute Node		
	Processor on compute node	(Lenovo/HCL): Request not to specify the CPU clock speed	We have only said 2.5 GHz or better. So better speed can be provided
	If these are Diskless nodes, all compute nodes should boot within 5 minutes from the time of issue of one single command from any of the master nodes.	(Locuz): Requesting IISER to consider only with Disk based solution	Not clear why such a request is made. Either configuration is acceptable. No change in specifications
4	PART - C Hybrid Note		
	Processor on Hybrid/GPU node	(Lenovo/HCL): Request not to specify the CPU clock speed	We have only said 2.5 GHz or better. So better speed can be provided
	NVIDIA Tesla GPU with a minimum performance of 1.0 TFLOPS without turbo boost (double precision mode) per GPU	(Locuz): Kindly amend the same with latest GPU card which is 12Gb memory and 1.3 TF in double precision	Says only minimum specification. Better performing cards can be offered at the vendor's discretion.

5	PART - D Node Redundancy (CPU only, spare)		
	Processor on spare nodes	(Lenovo/HCL): Request not to specify the CPU clock speed	We have only said 2.5 GHz or better. So better speed can be provided
	Enterprise SATA @ 7200 rpm, minimum 500 GB capacity.	(Locuz): Requesting IISER to include minimum 1 TB HDD	Reason for request not clear. 500 Gb is the minimum in the compute nodes also. Again 1 Tb offer also satisfies the specification
	Node redundancy - chassis	(Wipro): We request IISER to clarify, in case Chassis model servers model, if there is sufficient "U" space available in main compute chassis, can we use those chassis, or additional chassis also need to be proposed.	Yes, if sufficient U space is available those chassis can be used. However vendor is encouraged to design so that the nodes are indeed redundant in case the chassis itself fails - ie, separate chassis is strongly encouraged and such aspects will play a role in deciding the marks for "solution superiority".
6	PART - E MDS and OSS Nodes		
	MDS and OSS Nodes	(Lenovo/HCL): Please make this optional if storage solution does not require proposing OSS and MDS nodes.	We are not allowing alternate file systems (seen answer to question later)
7	PART - F Storage		
	Luster Failover and MMP (Multiple Mount Protection) should be configured	(Lenovo/HCL): Requesting IISER to allow bidder quote alternate parallel file system like GPFS, iBrix, Nextstore only	Cannot be allowed. Requested file system has to be provided.
	MDT in RAID 10 and OST in RAID 6 (Hardware RAID). (8 + 2) configuration	(Locuz): Rephrase the statement (for clarity) as Hardware RAID for MDT also	Rephrased: MDT in RAID 10 (Hardware RAID) and OST in RAID 6 (Hardware RAID). (8 + 2) configuration
	OST should be in separate storage enclosure/s, which is/are connected to the OSS server	(Locuz): Please clarify that do you need separate storage for OST and MDT or different disk enclosure for OST and MDT	The specification is clear that the OST and MDT are in different disk enclosures. Remains as such with no change.
		(Wipro): We request IISER to clarify, if same	We believe that the specification is clear

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		storage controller can be shared for OST and MDT storage using dedicated storage enclosure for MDT storage.	and separate nodes/controllers etc are needed for OST/MDT. Same storage controller cannot be used for both.
	500 TB usable capacity storage on SATA/NL- SAS or SAS with Hardware RAID 6 (8 + 2) storage array with a minimum of 5 GB/s write performance. Read performance should not be less than write	(HP): Does this mean 5GB/s write + 5GB/s read throughput (10GB/s overall) at any given time?	While writing we require more than 5Gbps. Read speed must be more than 5Gbps. We do not need 5+5 at any given time.
	PART - G Software		
8	Job Scheduler & Resource Manager - Can be Open Source.	(Altair): We request the committee to consider Licensed and Commercial Software for this requirement, the current RFP will ensure all the vendors to quote Open Source components only.	No, Since OS etc are open source, we are not insisting on commercial software for scheduler alone. If the bidder is including an open source Job Scheduler & Resource Manager in the bid, then the bidder MUST quote for a commercial Job Scheduler & Resource Manager as an OPTIONAL item. Details of the software must be included in the technical bid with the cost, including licensing charges etc for six years listed as an optional item in the commercial bid. If IISER TVM decides to exercise this option based on evaluation of the technical bid, then the price of the optional item will be included in the price comparison. The bidder is free to quote ONLY a commercial version as part of the main bid if such software is necessary according to the bidder to attain the listed performance benchmarks.
	Software's like Linux OS, Resource Manager, Job Scheduler and File	(Lenovo/HCL): Software's like Linux OS, Resource Manager, Job Scheduler and File	Commercial software is allowed but we will not insist on ONLY commercial


 9/7/15 Page | 4

	system	system	software. If the bidder is including an open source Job Scheduler & Resource Manager in the bid, then the bidder MUST quote for a commercial Job Scheduler & Resource Manager as an OPTIONAL item. Details of the software must be included in the technical bid with the cost, including licensing charges etc for six years listed as an optional item in the commercial bid. If IISER TVM decides to exercise this option based on evaluation of the technical bid, then the price of the optional item will be included in the price comparison. The bidder is free to quote ONLY a commercial version as part of the main bid if such software is necessary according to the bidder to attain the listed performance benchmarks.
	PERPETUAL LICENSE is required for all these softwares	(HP): In the table given in page 7, it is mentioned as "Can be open source". Open source software does not need license, please clarify this. (CMS): Please remove the clause as Perpetual licenses are not available with Open source products(OS/ Job scheduler/ Resource Manager) as per RFP Requirement	Yes, agreed. For open source, non-licensed software, perpetual license is not needed. However support for the software is assumed to be included in the warranty and AMC
9	PART - H Server rack details		
	Useful cooling output (in kW) 30 kW	(HP): In page 2, under "Specification and Technical Details:" it is mentioned maximum power consumption as 20KW per rack. Please clarify whether 20KW or 30KW to be considered	20 KW to be considered.

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10	PART - I Applications that will run		
11	PART - J Network/Interconnect		
	4X Dual-QDR/FDR/XDR/equivalent-or-higher (in terms of bandwidth), Chassis switch with redundant power supply and redundant fan, HCA cards, cables, et	(Lenovo/HCL): Should we perceive this request as there should be dual QDR/FDR/XDR ports on proposed Servers in HPC solution proposed by bidder which also means dual physical connection from infiniband switch to each server. Please confirm	We remove the "Dual-QDR" as an option from this specification. That is, we modify this specification to - "FDR/EDR/equivalent-or-higher (in terms of bandwidth), Chassis switch with redundant power supply and redundant fan, HCA cards, cables, etc."
		(Locuz): Please confirm, there is no infiniband standard defined as XDR. We believe it is a typo and the RFP means EDR (100 GBPS)	Yes, XDR is a typo. Should be read as EDR
		(Locuz): As per the RFP is dual-Rail-QDR/FDR/EDR are being considered as equivalent offering then we would request to specify EDR as 1:2 blocking. This will ensure all solutions are on a level playing field, otherwise bidders will never propose any EDR option as it will be inherently more expensive.	We do not intend to consider all offerings as equivalent. If a vendor offers above the minimum then they score extra points in the solution superiority category. Also the design has to be such that the performance benchmarks that are set are satisfied.
		(Locuz): Dual Rail QDR should be built with two single port QDR HCAs per server as then only they bandwidth from the ports can be clubbed. A dual port single card will be bottlenecked by the PCIe bandwidth limitation and having dual-rail on switching side will be redundant.	We remove the "Dual-QDR" as an option from this specification. That is, we modify this specification to - "FDR/EDR/equivalent-or-higher (in terms of bandwidth), Chassis switch with redundant power supply and redundant fan, HCA cards, cables, etc."
		(HP): QDR, FDR are the technologies available in Infiniband. FDR delivers 560Gbps against QDR's 40Gbps bandwidth. Similarly commercially there is difference	We do not intend to consider all offerings as equivalent. If a vendor offers above the minimum then they score extra points in the solution superiority category. Also the


 9/7/15 Page | 6

		between QDR and FDR. Hence requesting to consider FDR as a technology for better performance. Also please clarify whether two ports per compute nodes to be used connected to the IB switch	design has to be such that the performance benchmarks that are set are satisfied. Two single port QDR HCAs per server
	Master/Head/Login node to have 10Gbps NIC for LAN connectivity	(Locuz): Please specify if you need 10BASE T ports or SFP+ ports and how far the LAN switch so that accordingly cable length can be estimated.	SFP+ port is preferred.
12	Cluster Management		
13	Job Management		
15	Compilers Library and other software		
15	General terms and conditions		
	Delivery period will be 8 weeks from the date of purchase order. Once delivered to onsite, the installation, commissioning and acceptance testing period will be within 4 weeks from the date of delivery of equipment	(Lenovo/HCL): Requesting IISER to change delivery time frame as 8 weeks from date of LC opening by IISER and 8 weeks instead of 4 weeks for installation, commissioning and acceptance testing	The time period will be changed as requested.
		(Wipro): We request IISER to change the term to delivery period 8 weeks from date of opening of LC	Agreed
		(Locuz): Requestion you to consider the delivery period from 8-10 weeks from the date of clear LC. Installation 4 more weeks and 2 more weeks for reproducing benchmarks	Agreed
	General Terms and Conditions: The installation should be done by certified and trained engineers for HPCC stack (e.g. Parallel file system,	(HP): How many days of training to be provided and for how many users. We suggest IISER to include the training on Parallel programming and code optimization	Minimum 2 weeks initial training for around 5 to 10 users. Yes, training on parallel programming, code optimization techniques, GPGPU programming etc

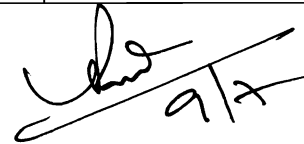
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	Infiniband etc.) followed by comprehensive user training.	technics which helps better utilization of cluster by user community.	should be included.
16	Warranty/Checklist		
	The bidder should also clearly indicate post-warranty comprehensive AMC cost including taxes for a period of another 3 years, on an annual basis, in the Commercial bid.	(Lenovo/HCL): Will AMC charges be used for deciding the L1 bidder. Please confirm	Yes, AMC charges for the additional 3 years will also be considered in the bid comparison
	The above tests should be repeated after 30 days without rebooting the cluster, in presence of the Bidder/OEM and IISER-TVM team. Post-30 days performance must be demonstrated to be the same as at the time of installation. IISER-TVM team can insist to do the tests by themselves in presence of the Bidder/OEM	Will this cluster be idle for these 30 days or users will continue production runs. If this cluster been handed over to the users, then how to ensure that the environment is not changed in the 30 days period.	Cluster will not be idle. However only regular use including addition of users, running of jobs etc will only be done. Addition of software, software update etc will be avoided during this period to keep the overall software environment unchanged. The vendor can keep the root password for the 30 days and do the system administration on IISER TVMs behalf for the period.
17	Bidder eligibility criteria		
	The OEM should have had a presence in the TOP500 Supercomputer list (http://www.top500.org) continuously in all the lists of the years 2012, 2013 and 2014.	(Lenovo/HCL): Requesting IISER to remove this clause or please consider IBM entries in top 500 as Lenovo / IBM entries under the situation that IBM System X business got acquired by Lenovo starting October 2014 which completed globally only in Jan 2015 .	Yes, IBM system X can be considered for the eligibility. Proper documentation in addition to a statement by Lenovo to be provided.
		(CMS): "Request to include latest June 2015 List and amend this clause as following ""The OEM and SI should have had at least one entry in the Top Supercomputers-India list (<a 640="" 812="" 868="" 954"="" data-label="Text" href="http://topsupercomputers-</td> <td>Yes agreed, June 2015 list also to be considered and the clause thus amended. Only OEM need to have an entry in the top list</td> </tr> </table> </div> <div data-bbox="> <p><i>Handwritten signature and date: 9/11/15</i></p> 	

	<p>india.iisc.ernet.in) maintained by SERC, IISc, Bangalore between December 2012 (the ninth list) up to June 2015 (the fourteenth list).</p> <p>Justification: Since it is a big opportunity, SI has the important role to front end and the execute the HPC System, They should be experienced in the field .Also Including the latest 2015 June list would enable more OEMs to propose their product for this tender"</p>	
	<p>(Netweb): You have asked for one listing on the India Top 500 List with the same OEM, we would like to state that, we have couple of Installations with government reputed institutes, they prefer not to list on any such list for strategic reasons. We can provide proof of such successful installations which are of same or more capacity/performance of other entries on the list. We feel that this should be considered</p>	Not accepted
3)The OEM/bidder should have installed at least one supercomputer of 7 TFLOPS (Rpeak) capability (contribution from CPU-only nodes) in India. The same should have been successfully supported (post-installation) at least for a period of one year. Provide a certificate from the customer to this effect.	<p>(CMS): Request to amend this clause as "Both OEM as well as bidder (system Integrator) comply to this clause. Justification: For such a high level Implementation the System Integrator has a major role to play both for installation as well as after installation support. So the Implementing system Integrator also needs to have this experience.</p>	Not agreed
4) The OEM/bidder should have installed at least one supercomputer	<p>(CMS): "Request to amend this clause as ""Both OEM as well as bidder (system</p>	Not agreed


9/7/15

	in India with Infiniband (or equivalent or better) interconnect, with PFS and end-to-end sustained I/O throughput of at least 2 Gbps.	Integrator) comply to this clause. Justification: For such a high level Implementation the System Integrator has a major role to play both for installation as well as after installation support. So the Implementing system Integrator also needs to have this experience"	
18	Benchmark performance criteria		
	If the bidder has access only to a system which has higher performance than that mentioned above, then the bidder is expected to run the benchmarks on a subset of the system whose performance does not exceed the limits mentioned (10 TFLOPS for CPU cluster and 10 TFLOPS for GPU+CPU cluster). Also, the benchmark has to use a machine topology that exercises all the cores in each of the nodes in the subset.	(Lenovo/HCL): If this case, how the benchmark results will be evaluated? You have asked to run Benchmarks on a CPU only 10 TFCluster, we would like to request you to amend it to 5 TF, similarly for CPU + GPU Cluster to 5TF. As we have the facility available in our own lab.	We don't see a problem here. Necessary documentation to show that the benchmarks were indeed run on a subset of nodes of the right size has to be provided. The subset will be treated as equivalent to a system of the specified size. Not acceptable
	SCALAPACK Matrix Inversion using the routine 'PDGETRI.f' (http://www.netlib.org/scalapack/explore-html/d3/df3/pdgetri_8f_source.html) . The matrix should be a randomly generated general double precision square matrix of order n=100000.	(Lenovo/HCL): To have an uniformity across all the vendors, please share a common code which does the random number generation for matrix 10000 and calls PDGETRI subroutine (Locuz): What is the routine program to be used for the benchmark? Clarity required? (HP): Can "n" be more than 10000. For example can we run Scalapack benchmark	It is a randomly generated matrix. The vendor should have in house expertise to do at least this much. The instructions are quite clear and explicitly mentioned the routine to be used. We expect the vendor to have at least sufficient in house expertise to do this much. No, all vendors are required to report results with identical input parameters.



		with $n > 50000$	
	<p>SCALAPACK Matrix Inversion using the routine 'PDGETRI.f' (http://www.netlib.org/scalapack/exlore-html/d3/df3/pdgetri_8f_source.html) . The matrix should be a randomly generated general double precision square matrix of order $n=10000$ CUDA version of the code should be run on a CPU+GPU cluster having 10 TFLOPS of Rpeak</p>	<p>(Locuz): What is the routine program to be used for the benchmark? Clarity required? SCALAPACK for GPU version is not available in open source, What GPU library we need to use for the benchmark?</p>	<p>The routine is 'PDGETRI.f', clearly mentioned. The OEM/Vendor is expected to have the required library (the routine is not a very specialised one). Any library is okay.</p>
	Quantum Espresso and SCALAPACK	<p>(Lenovo/HCL): Can we use any version of QE for this benchmark? Do we need to have turbo on/off for the SCALAPACK/QE benchmark?</p> <p>(HP): Quantum espresso - can QE be run in Open MPI + MPI hybrid mode? Other technical queries on running QE</p>	<p>Any version is fine. Turbo OFF for the SCALAPACK/QE</p> <p>Yes it can be run in OpenMP + MPI hybrid mode for benchmarking. Note that, in the end QE has to run satisfactorily on the system being supplied and the vendor had to make it run.</p>
	The proposed solution has to deliver a sustained Rmax of at least '0.75 x Rpeak' with 80% of the TFLOPS coming from CPU only nodes (with turbo mode off) and the rest 20% TFLOPS coming from hybrid CPU-GPU nodes. Documentary proof to the effect that the proposed solution will meet this criterion has to be submitted along with the bid	(Locuz): What documents will suffice as proof of the effect that the proposed solution will meet this criterion of 0.75 x Rpeak? Request to separate the CPU only node benchmark and the GPU node benchmark	The subsequent points in the section "Benchmark performance criteria" of the tender elaborate on the documentation required. In addition other standard or custom benchmarking programs can be run by the vendor on the types of systems mentioned in the subsequent points and the results can be reported. Ultimately the onus is on the vendor to convince us that they have the capacity to build a well tuned system that can

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			deliver 0.75XRpeak performance. We have now specified that the GPU cards are not to be considered in the calculation of Rpeak and Rmax (please see the response to the query in SI No.1)
	Supporting documents: As specified in the benchmark performance criteria section	(Locuz): Please clarify "Calculation of Benchmark performance score", say with an example. Please explain "Aggregate normalized benchmark execution time factor"	The evaluation criteria are described in explicit detail in the tender. If an example is needed it will be orally discussed.
	Benchmark: Interconnect	(Locuz): In the whole document there is no clarity on interconnect which has to be proposed for the benchmark? We need clarity from IISER.	The type of interconnect we need for the system to be procured is clearly defined. We cannot define the system on which you have to run the performance benchmark criteria too rigidly since that would be rather unreasonable. The onus is on you to run the benchmarks on as similar a system as you can manage to the one that is being proposed in terms of node configuration, interconnect etc. Details of the system on which the benchmarks were run may be included in the bid for helping us also to understand better the scores and running times that you will be reporting.
19	Evaluation Methodology		
20	Commercial		
	Payment terms	(HCL): In case the site is not ready then IISER is willing to release the money due to the vendor or allow for delivery only when the site is ready.	Site will be ready by the time the hardware is delivered


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	(HCL): Considering the fact that not many HPC installations are their in Thiruvananthapuram, we suggest IISER to ask for a mandatory resident engineer at site. (Wipro): Considering the size of cluster, remote location and complexity in initial phase of maintenance, we suggest IISER to consider adding an onsite support engineer at least for one year for smooth operation of the setup.	All vendors may quote the additional cost of posting one resident engineer with at least one year experience with HPC installations for one year at site. This will be an optional item and its cost will be included in the commercial bid comparison only if IISER decides to exercise this option.
Payment terms: Letter of Credit. 90% against delivery, installation and 'Acceptance Test Check'. 10% after 'Performance Acceptance Test Check', after 30 days	(Locuz): In the RFP two payment terms are mentioned in the tender document (Page 23 and 36). Need clarity on correct payment terms for imported goods.	Letter of Credit. 80% payment against shipment, 10% after installation and Commission and on submission of BG for 10% of P.O value towards performance during warranty period and balance 10% after 30 days performance of system without rebooting.
For indigenous supplies payment will be made within 30 days from the date of supply, installation and commissioning. For imported goods, the preferred payment mode is ...	(Locuz): We are fine with the payment term of 100% irrevocable LC with 90% payable against submission of shipping document and balance 10% against installation and commissioning with submission of PBG of 10% of order value till warranty period	
EMD	(Wipro): Kindly mention the details of EMD: Whether DD/BG, it should be in favour of whom and payable at?	IISER, Thiruvananthapuram
Excise Duty: IISER-TVM is exempted from paying excise duty, under 10/97 notification	(Wipro): Wipro will be the System Integrator for this project offering products of other Original Equipment Manufacturers (OEMs). In case Wipro becomes L1, we would require IISER- Trivandrum to issue Excise Duty Exemption Certificate in the name of our suppliers/OEMs as they are the material manufacturers. However, PO	YES. On request of integrator EDEC will be issued on OEM/ Supplier. However, the integrator need to establish an evidence that the final destination of the items is IISERTVM,

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		should be placed on Wipro Ltd only. In case IISER cannot issue this certificate in our supplier's names, we will be quoting prices inclusive of Excise Duty and same has to be paid by IISER. Kindly confirm.	
	Integrity pact	(Locuz): Please confirm whether integrity pact to be submitted by bidder in letter head of technical bid	Integrity Pact may be printed on plain paper and submitted with the seal and signatures of OEM and Integrator/ Indian agents. This should be the part of Technical bid.
	Additional clauses to be added	(Wipro): Additional clauses suggested on limitation of liability, Site not ready, taxes and duties, deemed acceptance etc.	As per GFR and Manual on Policies and Procedures for Purchase of Goods Govt. of India.
	-	-	Out of two copies of tender (Technical) one can be a soft copy in the form of CD/pen drive.
	-	-	Refer Tender number in all correspondence


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[Purchase & Stores]

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उप कुलसचिव [क्रय एवं भंडार]
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