



**INDIAN INSTITUTE OF SCIENCE EDUCATION AND
RESEARCH THIRUVANANTHAPURAM [IISERTVM]**

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Date: 8th Nov 2016

ADDENDUM II TO TENDER NO
No: IISER/PUR/5790/15

Dear Sirs,

Sub: HPC Cluster- Responses to Pre-bid Queries

This has reference to Pre-bid meeting held on 20/09/2016 the queries raised by the Bidders. The responses of IISERTVM are available in Annexure I to this Addendum. Vendors are required to make note of these changes to our technical specifications and commercial terms of the tender notice No. IISER/PUR/5790/15 under advertisement No. IISER/PUR/PT/06/16 dated 20/09/2016.

Also Please note the

Last date for submission of tender is hereby extended upto 6th December 2016 [Upto 4 PM] and subsequently date of opening of Technical bids: 9th December 2016 [at 10.00 A.M]

Thanking You



Yours Faithfully

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**Annexure I to Addendum dt. 2/11/2016 against IFT No: IISER/PUR/5790/15 dated 20th Sep, 2016
Responses to queries raised in Prebid Meeting held on 19 Oct 2016 at**

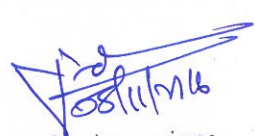
Subject	Relevant Spec	Query	Query From	Answer
Master node	Memory: RAM 4GB per core DDR4 2133 MHz	Request IISER TVM to assure the population of all the DIMMS while configuring the memory for better performance. Please modify 2133 MHz as 2400 MHz	Locuz	Specifications for RAM for the master node stands modified to: RAM 4 GB per core, DDR4 2400 MHz. All DIMMS used should be populated in a balanced configuration with identical memory modules.
		Master Node : Memory : RAM 128 GB per node, DDR4 2400 MHz. The per core memory allocation may properly configure the processor to memory mapping and may lead to imbalance. In turn will lead to degradation in performance. The per node allocation either with 64 GB, 128 GB , 256 GB , 512 GB will allow to get the best <u>balanced performance</u>	Lenovo	
		The memory configuration being asked for master/login/computes/Hybrid nodes/IO nodes/etc is 4GB DDR4 per core. In case there is usage of multiple size memory DIMMs (to achieve 4GB per core), there could be performance drops in memory bandwidth. Hence, we assume all the memory DIMMs should be of the same size and appropriate number of slots to be filled to get the best performance. Request your clarification on the same.	Cray	
	4 X min 500 GB enterprise HDD SAS @ 10000 rpm	Request to modify the capacity to 600GB SAS 10K RPM	Locuz	Specification for Hard Disk for the master node stands modified to: 4 x minimum 600 GB Enterprise Hard Disk SAS @10000 RPM
Compute Node	Memory: RAM 4GB per core DDR4 2133 MHz	Request IISER TVM to assure the population of all the DIMMS while configuring the memory for better performance. Please modify 2133 MHz as 2400 MHz	Locuz	Specifications for RAM for the compute nodes stands modified to: RAM 4 GB per core, DDR4 2400 MHz. All




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		CPU Only Compute Node : RAM 128 GB per node, DDR4 2400 MHz: The per core memory allocation may properly configure the processor to memory mapping and may lead to imbalance. In turn will lead to degradation in performance. The per node allocation either with 64 GB, 128 GB , 256 GB , 512 GB will allow to get the best <u>balanced performance</u>	Lenovo	DIMMS used should be populated in a balanced configuration with identical memory modules.
		The memory configuration being asked for master/login/computes/Hybrid nodes/IO nodes/etc is 4GB DDR4 per core.In case there is usage of multiple size memory DIMMs (to achieve 4GB per core), there could be performance drops in memory bandwidth. Hence, we assume all the memory DIMMs should be of the same size and appropriate number of slots to be filled to get the best performance. Request your clarification on the same.	Cray	
	Redundant power supply	Modify the specification to allow redundant power and fan either at node level or chassis level as the case may be.	Oral query at the pre-bid	Specification for the compute node, A.4.a) on page 6 stands modified to: Hot pluggable, redundant power supply and redundant fan units to be provided at for each node or chassis as the case may be
Hybrid node	GPU/Accelerator: NVIDIA Tesla K80 GPU	Request IISERTVM to keep K80 Card must be in 1:1 ratio with processor to assure the faster execution of CPU based instruction being executed in the hybrid program	Locuz	The GPU hybrid nodes specifications are modified by adding the following condition: In a GPU node, the number of K80 Cards must not exceed twice the number CPU cards.
	Memory: RAM 4GB per core DDR4 2133 MHz	Request IISER TVM to assure the population of all the DIMMS while configuring the memory for better performance. Please modify 2133 MHz as 2400 MHz	Locuz	Specifications for RAM for the hybrid nodes stands modified to: RAM 4 GB per core, DDR4 2400 MHz. All




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	<p>The memory configuration being asked for master/login/computes/Hybrid nodes/IO nodes/etc is 4GB DDR4 per core. In case there is usage of multiple size memory DIMMs (to achieve 4GB per core), there could be performance drops in memory bandwidth. Hence, we assume all the memory DIMMs should be of the same size and appropriate number of slots to be filled to get the best performance. Request your clarification on the same.</p>	Cray	<p>DIMMS used should be populated in a balanced configuration with identical memory modules.</p>
	<p>Hybrid Node : RAM 128 GB per node, DDR4 2400 MHz: The per core memory allocation may properly configure the processor to memory mapping and may lead to imbalance. In turn will lead to degradation in performance. The per node allocation either with 64 GB, 128 GB , 256 GB , 512 GB will allow to get the best balanced performance.</p>	Lenovo	
<p>Alternative: "Intel Xeon Phi Processors" Nodes: Memory: RAM 4GB per core DDR4 2133 MHz</p>	<p>Since KNL comes with self booting node and performance is dependent of MCDRAM (16 GB) + RAM request to modify the RAM requirement as 96GB RAM</p>	Locuz	<p>Memory specification for the Alternative, Intel Xeon phi processor nodes stands modified to: For Intel Xeon Phi 'Knights landing', nodes, 96 GB of DDR4 2400 MHz RAM to be supplied with identical dimms in a balanced configuration in addition to the on-package 16 GB MCDRAM.</p>




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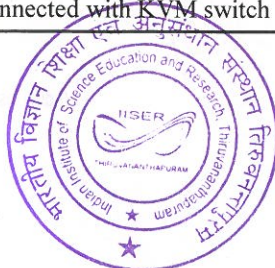
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Node redundancy (CPU - only compute spare nodes)	These nodes must be present as part of the cluster and powered on always. Spare nodes should be installed with operating system and should be integrated with the network	Please clarify these nodes need to be quoted with job schedule and cluster management license.	Locuz	Specification A.6.a) on page 7 stands modified to: These nodes must be present as part of the cluster and powered on always. Spare nodes should be installed with operating system and should be integrated with the network. However the redundant nodes need not be included in the job scheduler and cluster management license number.
IO Nodes (MDS and OSS)	Memory: RAM 4GB per core DDR4 2133 MHz	Request IISER TVM to assure the population of all the DIMMS while configuring the memory for better performance. Please modify 2133 MHz as 2400 MHz	Locuz	Specifications for RAM for the MDS/OSS nodes (In case Luster PFS) stands modified to: RAM 4 GB per core, DDR4 2400 MHz. All DIMMS used should be populated in a balanced configuration with identical memory modules.
	OEM supported Lustre	Request you to make it as optional as required by the solution for the optimal system configuration.: There are equivalent technology and Parallel File System which does not need dedicated MDS and OSS nodes. This requirement is just confined to Lustre Parallel File System.	Lenovo	
	MDT size	Request to put the minimum size of MDT at 2%	Oral query at the pre-bid	
Management switch	Master/Head/Login node has to have 10Gbps NIC for LAN connectivity	Please clarify how many and what type of port would be needed.	Locuz	10 Gbps connectivity through RJ45 (copper) ports.




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	A9. Network Interconnect	Based on the cluster size it is recommended to have the separate network for Management, Pls. clarify.	Lenovo	Separate management network may be proposed as part of the system design by the vendor. No changes to the specifications.
Storage	Storage nodes and management nodes should be connected with KVM switch and display	Please curllily on the KVM switch number of port required (8 port or 16 port/ Analog or digital) and monitor whether you are looking for 17" LCD console or min 19" TFT monitor	Locuz	KVM switch details to be decided by the vendor based on the design of the proposed solution.
	500TB usable capacity storage on SATA/NL-SAS or SAS with hardware Raid 6 (8+2) storage array with minimum of 5GB/s write throughput performance. Read Performance should not be less than write.	500TB usable capacity storage on SATA/NL-SAS or SAS with Raid 6 storage array with minimum of 5GB/s write throughput performance. Read Performance should not be less than write.	HP	Hardware raid is preferred. No changes to the relevant specification. Request to change is not accepted.
	Storage Configuration : 1. Parallel File System (PFS) - Intel Sourced and OEM supported Lustre. 2. The solution should be highly available and with no single point of failure , including I/O servers, Metadata servers, Storage Array, HCA Cards and power supply. 3. MDT in RAID 10 and OST in RAID 6 (Hardware RAID). (8+2) configuration. 4. MDT in RAID 10 and OST in RAID 6 (Hardware RAID). (8+2) configuration. 5. Global Hot Spare Disks : Disks amounting to 5 % of total capacity of Enterprise SATA/NL-SAS or SAS provided as Global Hot Spare (ie. Global Hot Spare for every 2 LUN in RAID 6).	Storage Configuration : 1. Parallel File System (PFS) - Intel Sourced and OEM supported Lustre or IBM supported GPFS. 2. The solution should be highly available and with no single point of failure , including I/O servers, Metadata servers, Storage Array, HCA Cards and power supply. 3. In case Lustre PFS is proposed MDT in RAID 10 and OST in RAID 6 (Hardware RAID). (8+2) configuration. 4. MDT in RAID 10 and OST in RAID 6 (Hardware RAID). (8+2) configuration. 5. Global Hot Spare Disks : Disks amounting to 5 % of total capacity of Enterprise SATA/NL-SAS or SAS provided as Global Hot Spare (ie. Global Hot Spare for every 2 LUN in RAID 6). 6. Storage Throughput - Minimum 5 GB/s write speed from compute nodes. 7. In case Lustre PFS is proposed the OST should be separate storage enclosure/s which is /are connected to the OSS servers. 8. In case Lustre PFS is proposed the MDT Harddisk should be SAS 10000 RPM or higher. 9. Storage nodes and Management nodes should be connected with KVM switch	Lenovo	The point numbers 1, 3, and 8 in "Storage Configuration" are modified as follows. 1. Parallel File system (PFS) – Must be OEM Supported (Intel Luster or IBM GPFS). Bidders to include appropriate authorization letter from manufacturer for the same. 3. In case Luster PFS is proposed MDT in RAID 10 and OST in RAID 6 (Hardware RAID). (8 + 2) configuration. 8. In case Lustre PFS is proposed the MDT Harddisk should be SAS 10000 RPM or higher.



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Interconnect	FDR (56 Gb/s) infiniband (IB), chassis switch with redundant power supply and redundant fan , HCA cards cables etc.	100Gb/s high performance fabrics in chassis model with redundant power supply and redundant fan, adapters, cables etc	HP	Specifications listed under section A.9 for the network/interconnect stand changed as
	The solution should have fully non-blocking interconnect fabric FDR topology using a single chassis switch.	The solution should have fully non-blocking interconnect fabric using a single chassis-switch.	HP	Infiniband fabric interconnect 1. EDR Infiniband or Omnipath interconnect at 100 Gbps in fully Nonblocking configuration. The switch should have adequate number of ports to allow expansion of another 20 additional compute nodes in future.
	Infiniband Fabric Interconnect : 1. FDR (56 Gb/s) infiniband (IB), Chassis switch with redundant power supply and redundant fan, HCA cards, cables etc. 2. The solution should have fully non-blocking interconnect fabric FDR topology using a single chassis switch.	Infiniband Low Latency Fabric Interconnect for Computation : 1. FDR (56 Gb/s) infiniband (IB) Minimum 56 Gb/s low latency Interconnect distributed / Chassis switch with redundant power supply and redundant fan, HCA cards, cables etc. 2. The solution should have fully non-blocking interconnect fabric FDR topology using a single chassis switch. Recently Intel launched OmniPath which is equivalent to Infiniband but special type of interconnect. The latest version of Mellanox Interconnect supported in Broadwell processor based system is EDR. Both EDR and Intel OPA are at par and can deliver a end to throughput of 100 GB/s at 1.2 µs max.	Lenovo	Management Switch a) Master/head/login node has to have 10Gbps NIC (RJ45, copper) for LAN connectivity b) All nodes to be connected by gigabit network for administrative works.
		The Infiniband Fabrics being specified is Mellanox FDR (56 Gb/s) with Chassis Switch which is now an older technology. As the processors being asked in the RFP is Intel Broadwell, we request you to change the interconnect specifications to Mellanox EDR or Intel Omnipath which are the latest available and higher bandwidth (100 Gbps) to get the optimal performance. Else Request your consideration of the same.	Cray	
Software	IISER TVM Clarification		The Intel Parallel Studio XE 2016 (Cluster Edition for Fortran and C/C++) -	

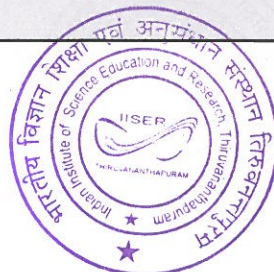


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Perpetual license is required for all these software	Please clarify if IISER TVM is looking for commercial software only or we can quote with open source options also.	Locuz	Both commercial and open source are allowed as per the specifications. Tendered specifications remain unchanged and have to be followed as such
Software : File System - OEM supported Lustre	Software : File System - OEM supported Lustre or IBM Supported GPFS This will allow us have an option to quote our own vendor file system.	Lenovo	As mentioned above in "Storage Configuration" the specification is modified as "Parallel File system (PFS) – Must be OEM Supported (Intel Luster or IBM GPFS). Bidders to include appropriate authoriza on le er from manufacturer for the same."
High Availability test with anyone MDS down and anyone or more OSS down.	High Availability test in case of Lustre with anyone MDS down and anyone or more OSS down and in case of GPFS IO node down This will allow GPFS to qualify the UA Checklist.	Lenovo	
Management tool	The cluster management tool/software being asked is based on open source stack and doesn't have the necessary features to manage and monitor a large system size of 120 TFLOPS. We hereby request you to change the same to commercially supported GUI based cluster management tool which will ease the cluster management and monitoring.Request your consideration of the same.	Cray	The section A.10 is modified by adding the following software component specification: Cluster Management Tool: GUI based cluster management tool with the following features: 1. Discovery of hardware on the network. 2. Update of firmware, and BIOS/UEFI settings. 3. Remote management of the hardware: power on/off, console, boot order control, inventory, vpd data.
Job Scheduler	Job management - restricting user login to compute nodes - request to drop this clause to allow more open-source solutions.	Oral query at the pre-bid	Specifications in section A11 on page 9, under subsection 'Job management' stand modified. Point number 13: Restricting user login to compute nodes is no longer a required feature of the job management suite



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	Intel Cluster Studio XE	Request to clarify the duration of Intel compiler support required.	Oral query at the pre-bid	Intel compiler support for three years to be included in the quote.
Benchmark, eligibility etc.	IISER TVM Clarification			Second sentence of Item 1 in Table D.2.1 on
	Benchmark Performance criteria: Point 1 - last line says "Documentary proof to the effect that proposed solution will meet this criterion has to be submitted along with bid". (For Rmax /Rpeak =0.75 clause)	Can we submit TOP500 listing on similar or higher system as documentary proof? Please clarify	HP	Yes, TOP500 listings may be provided as documentary proof.
	Quantum Espresso. The Program "Medium-size benchmark for pw.x" described in http://qe-forge.org/gf/project/qe/frs/?action=FrsReleaseView&release_id=44 should be run on a CPU+GPU cluster with 10 TFLOPS of Rpeak. The binary 'pw.x' should be built with GPU support enabled. Patches of GPU enabled versions are available at https://github.com/fspiga/QE-GPU The 'total WALL Time spent' in the output file will be considered for evaluation purpose.	The asked benchmark is just for CPU and GPU. If the accelerator proposed is Xeon Phi then also is it mandatory to do benchmark on GP-GPU ?	Lenovo	The benchmark test for CPU-GPU hybrid to be done on CPU-GPU node(s) even if the proposed solution is using the alternative, namely Intel Xeon Phi.
Scalapack	Is there any more detail on this? Can we use intel Scalapack or do we have to use netlib.orgs? What do they want reported here (Wall clock time?). Can we use whatever we want to generate the n=100k matrix?	Dell	Yes, netlib version to be used. Wall time to be repeated. The required size of the matrix to be used for inversion is raised to 1,000,000 from 100,000.	



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Quantum Espresso	Is there a specific version of QE to be used? QE 5.4.0 is the latest and it doesn't have the "total wall time spent" parameter. Instead it has the "total cpu time spent parameter". If they want wall time, I can use "time" to get that as well.	Dell	Version of QE to be used and the items to be reported is already specified in detail in the tender.
Quantum Espresso	The GPU patch is in official QE 5.4.0 release. DO we have to use the github one or can we use the one from QE's site?	Dell	GPU patch may be obtained from gitbub or QE site.
Page 15: D.1.Bidder's Eligibility Criteria. Point 3:	As the OEM/Bidder's eligibility criteria its been asked " The bidder should have installed at least one supercomputer of 7 TFLOPS (Rpeak) capability in India." Keeping in view the size of the cluster requirement as per the RFP (120 TFLPS), reference of 7 TFLOPS system doesn't justify capability of the respective vendors to install successfully a large system. Hence we hereby request you to change the above criteria to minimum 100 TFLOPS size installed customer reference. Request your confirmation on the same.	Cray	Not accepted. The critera specifications remain unchanged.
Page 16 & Page 17: D.2/D.2.1: Benchmark Performance Criteria:	As per the application Benchmarking criteria, it has been asked to run the benchmarks on a 10 TFLOPS (Rpeak) system, which does not justify the capability of the bidder/OEM in terms of installing a large system of 120 TFLOPS with necessary optimization required, application scalability/performance, etc. Hence we request you to change it to run the application benchmarking on at least 100 TFLOPS or similar system (Rpeak) to ensure all the participating vendors are capable to install large systems and run & execute application benchmarks in terms of performance and scalability. Request your consideration of the same.	Cray	Not accepted. The critera specifications remain unchanged.



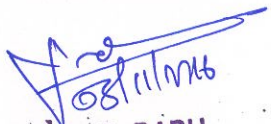
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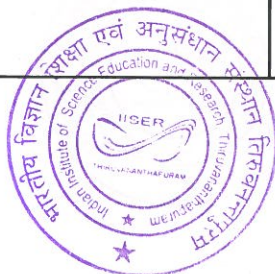
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Page 18 & Page 19: Point E.2 & Point 7:	As per the Point no. E.2 & Point.7, the evaluation is based on a scoring scheme but is only limited to to the technical bid. As per the same the highly technical scoring vendors might not be lower on prices. Hence this might land up on consideration of vendors who has scored lower technically and with lesser prices. To make it even for all vendors we request you to make the technical scores of all the qualified bidders also to be used as part of the techno-commercial evaluation with inclusion of commercial bid part of the scoring scheme. Request your consideration of the same.	Cray	The requested change would require a comprehensive change in the tender as far as bid evaluation and award of work is concerned. These changes are not possible at this stage of the tender. Hence the request is not accepted.
	We request you to include presentation of the technical bid to be part of the evaluation criteria as well, wherein the bidders can present their solution pertaining to HPC and Datacenter (Being a complex turnkey project). And also all necessary clarifications and queries by IISER can be answered during the presentation. Request you to consider the above.	Cray	No further technical presentations are scheduled.
Data center	IISER TVM Clarification		Point 5 in table D.1: Bidders' Eligibility Criteria on page 16 is clarified as "The proposal can be submitted either by the OEM themselves or through and Authorised Partner. In case of the latter, a certificate of authorization by the OEM needs to be provided. Each HPC OEM should not participate in the tendering process with more than one Authorised Partner. The last condition does not apply to the data center components."




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State of the art Data center housed in Lab/room of approx 700 sqft area.	Please confirm 700sqft area is only for server room or the same part of the room should be utilised for keeping UPS, batteries & Electrical panels.	HP	The room available presently is 700 sq ft total including space for UPS, batteries etc. A site visit can be requested by email to purchase@iisertvm.ac.in prior to bidding if required
Sufficient 42U server racks with glass front door	Please confirm the number of High Density Racks required apart from additional 2nos. Of rack	HP	Confirmed. 2 additional racks
	Please confirm the dimension of the each rack width & depth.	HP	Additional rack dimensions identical to the ones proposed for the HPC nodes as per the design of the system
The HPC racks to have sufficient IP based metered PDU as per the HPC solution while the additional racks should have two IP based metered 3 phase PDU each havin 24nos. Of C13 & 6nos of C19 socket with 32A rating.	Please confirm you need 3phase PDU or single phase PDU for HPC racks	HP	Identical provision for power in the additional racks as in the HPC nodes
At least 10% extra cooling capacity to be built in to the solution provided	Please confirm 10% extra cooling you need in chiller & In Row or only in In Row.	HP	10% extra cooling for both chiller and in-Row/In-rack
External Power with DG Power backup will be provided at the chiller location. Since the Power backup for the chillers will be the generator and after shutdown there would be time lag before the chillers come on	Please confirm DG should be with Auto start & Power should resume to chiller within 30seconds during the raw power failure	HP	DG is with auto start
Comfort AC of sufficient capacity must be provided for the UPS room augmenting AC that may be available at the installation site	Please confirm UPS room comfort cooling shall be provided by bidder or you will provided	HP	Comfort A/C is available in the proposed site. Any augmentation required to accommodate the additional heat load of UPS etc in the scope of the supplier.



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The UPS unit provided should be modular	Please confirm you need two set of modular UPS or One set of UPS	HP	Modular UPS, one set, with n+1 redundancy.
Battereis must come with 3yrs warranty	As per manufacturer batteries will be with one year standard warranty. Kindly change it to one year warranty	HP	Batteries to be 3 year rated with one year replacement warranty.
CCTV monitoring and Access control to the racks and server room should be provided	Please confirm CCTV shall be located to monitoring the server room entry, front & rear side of the rack row. Apart from this any other area to be monitor . Also We recommend to monitor the UPS room also.	HP	Respective vendors to decide the CCTV system design and locations as required by the HPC solution proposed.
	Please confirm Access control you need Biometric or card reader. Also confirm you need it for individual rack or common.	HP	Access control through mifare card readers with control per row or per rack depending on whether in-row or in-rack cooling is proposed.
All parameters includin sensor readings, cooling system status, node status, physical state etc should be monitored and controlled	Request to confirm that all the equipment status can only be monitored remotely. You cannot control it & it is not recommended.	HP	Only remote monitoring required. Remote control is not required.
Faulty parts should be replaced within 48 hrs of logging a call	Request to confirm that all the parts cannot be replaced with in 48hrs. Kindly extend it to 72hrs	HP	Request not accepted. Replacement timeline stands as such
Bidder should also clearly indicate post warranty comprehensive AMC cost	Request to confirm that CAMC will not cover consumables like batteries, Fire suppression GAS, oi	HP	Data center AMC will not cover consumables like batteries, fire suppression gas and oil beyond standard warranties.
Sufficient 42U server racks with glass front door	In the rack specification, it has been asked to supply front glass doors which might hinder the cooling of the cluster. Hence we request you to change the specification to perforated front doors for better cold air circulation. Request your clarification on the same.	Cray	In row or In rack cooling solution may be proposed. Glass or metal doors are acceptable.

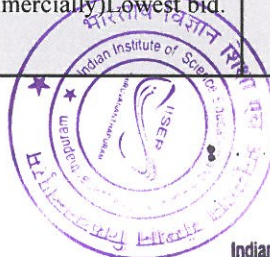


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		Request to include one more manpower as night resource of the datacenter with commensurate qualification (ITI diploma etc)	Oral query at the pre-bid	Item 1 in section A.13: Services on page 10 modified as follows: A service engineer for the HPC and a night resource for data center running, monitoring and maintenance is required to be stationed at site for a period of one year
Commercials	INCOTERMS: FOB/FCA/CIP	Please clarify how will the evaluation be done for the parent quoting in FOB/FCA, Singapore vs partner quoting in CIP Trivandrum. How many percentages will be loaded for the equal comparison on the incoterms	Locuz	Please refer Clause 1.11.2(B) of the Annexure I (Instructions to Bidders). For the purpose of evaluation forex rates of the tender opening date will be made as the benchmark. All the quotes will be evaluated on the same benchmark i.e either CIP TVM or Ex-Works or other incotems
	Delivery period will be 8 weeks from the date of PO. Once delivered on site, the installation commissioning and acceptance testing period will be within 4 weeks from the date of delivery of equipment	Requesting you to consider the delivery period from 8-10 weeks from the date of clear and negotiable LC at our bank and installation minimum 12-16 weeks from the date of delivery of material with site readiness. We are requesting minimum 12-16 weeks since datacenter portion also involved and we need to reproduce benchmark results also.	Locuz	Point 3 of Section C.2: General terms and conditions on page 15 stands modified as follows: Delivery period will be 8 weeks from the date of LC. Once delivered onsite the installation, testing, commissioning and acceptance testing period will be within 12 weeks.
	Installation, Commissioning and acceptance testing period will be within 4 weeks from the date of delivery	Request you to increase the time to 8 weeks for installation commissioning & ATP after delivery.	HP	
	Page 14: C.1.6: Warranties: AMC Cost	The system quoted should be with 3 years warranty. Also it has been asked to quote for additional 3 years AMC cost for HPC cluster and Datacenter in the commercial bid. Request your confirmation, if the AMC cost will be considered for evaluation of the (techno-commercially/commercially) Lowest bid.	Cray	AMC cost (3 + 3 years) will be considered for evaluation of the lowest bid.



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	Request to clarify whether the AMC cost included in commercial evaluation	Oral query at the pre-bid	
Payment Terms: 90% on installation & acceptance test check 10% after performance test check, after 30 days	We request IISER, TVM to consider 90% on delivery & 10% after performance test check	Wipro/H P	NOT Accepted
Request for modification of several of the general conditions of the tender that are common to all tenders floated by IISER TVM, including penalty clause, patent indemnity, termination for default etc.		Wipro/H P	Not accepted. All conditions and statements in Annexures I and II listing instructions to bidders and general terms and conditions of contract remain unchanged.



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